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				First Named Inventor	Yukimasa NAGAI
				Art Unit	2616
				Examiner Name	B. H. Pham
Sheet	1	of	1	Attorney Docket Number	2611-0246PUS1

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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/B.P./	BA	WO 2004/006444 A1	01-15-2004		T ⁶

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
/B.P./	CA	Matsumoto et al., "LDPC coded Hybrid Type II ARQ System," Symposium on Information Theory and Its Applications, pp. 273-276, (2003), XP002990209			
/B.P./	CB	Matsumoto et al., "Irregular Low-Density Parity-Check Code Design Based on Euclidean Geometries," IEICE TRANS. FUNDAMENTALS, Vol. E86-A, No.7, pp. 1820-1834, (2003), XP001174812			
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/B.P./	CD	Matsumoto et al., "Irregular extended Euclidean geometry low-density parity-check codes," International Symposium on Communication Systems Networks and Digital Signal Processing," pp. 148-151, (2002), XP0022370884			
/B.P./	CE	Matsumoto et al., "Fundamentals and Applications of Construction Methods of Low-Density Parity-Check Codes," Institute of Electronics, Information and Communication Engineers Society Taikai Koen Ronbunshu, pp. SS17-SS18, (2003), XP002998474			
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/B.P./	CG	Matsumoto et al., "Irregular Low-Density Parity-Check Code Design based on Integer Lattics," IEEE International Symposium on Information Theory, pg. 3, (2003), XP010657031			
/B.P./	CH	Chung et al., "Analysis of Sum-Product Decoding of Low-Density Parity-Check Codes Using a Guassian Approximation," IEEE Transactions on Information Theory, Vol. 47, No. 2, pp. 657-670, (2001), XP002969535			

Examiner Signature	/Brenda Pham/	Date Considered	10/07/2008
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